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Citation: *Applied Physics Letters* **104**, 052108 (2014); doi: 10.1063/1.4864471

View online: <http://dx.doi.org/10.1063/1.4864471>

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## Tunnel barrier engineering of titanium oxide for high non-linearity of selector-less resistive random access memory

Sangheon Lee, Jiyong Woo, Daeseok Lee, Euijun Cha, Jaesung Park, Kibong Moon, Jeonghwan Song, Yunmo Koo, and Hyunsang Hwang<sup>a)</sup>

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(Received 19 December 2013; accepted 25 January 2014; published online 7 February 2014)

In this study, the effect of the oxygen profile and thickness of multiple-layers  $\text{TiO}_x$  on tunnel barrier characteristics was investigated to achieve high non-linearity in low-resistance state current ( $I_{\text{LRS}}$ ). To form the tunnel barrier in multiple-layer of  $\text{TiO}_x$ , tunnel barrier engineering in terms of the thickness and oxygen profile was attempted using deposition and thermal oxidation times. It modified the defect distribution of the tunnel barrier for effective suppression of  $I_{\text{LRS}}$  at off-state ( $1/2V_{\text{Read}}$ ). By inserting modified tunnel barrier in resistive random access memory, a high non-linear  $I_{\text{LRS}}$  was exhibited with a significantly lowered  $I_{\text{LRS}}$  for  $1/2V_{\text{Read}}$ . © 2014 AIP Publishing LLC.  
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Resistive random access memories (ReRAMs) have been considered next-generation non-volatile memories, capable of solving the scaling limit problem of conventional flash memory.<sup>1–4</sup> ReRAM is a simple structure with a two terminals, and its high-density cross-point array is expected to be able to replace Flash memory.

However, the sneak path current which is the interference from neighboring cells can degrade the readout margin of a high density cross-point array. This sneak path current is caused by a high low-resistance state current ( $I_{\text{LRS}}$ ) at the off-state ( $1/2V_{\text{Read}}$ ). Current can flow to the low-resistance state ( $R_{\text{LRS}}$ ) cell during the reading operation for high-resistance state ( $R_{\text{HRS}}$ ) cell. To suppress the sneak path current, various selector devices—metal insulator transition (MIT), ovnonic threshold switching (OTS), mixed ionic-electronic conductors, and exponential switching—have been investigated.<sup>5–8</sup> In case of MIT devices, it still retains high operating current for its selectivity. The exponential selector devices have significantly low operating current for high-density cross-point array applications with low sneak-path current. But, they exhibit also low current at on-state for  $I_{\text{LRS}}$  of ReRAM. They cannot satisfy operating current ( $I$ ) and voltage ( $V$ ) of ReRAM for 1selector and 1ReRAM (1S1R). In case of OTS, it has very complicated materials composition for its reliable operation. In addition, these selector devices can increase the number of stacked layers in 1S1R devices fabrication. Furthermore, selector devices have required operating  $I$ – $V$  matching with ReRAM operating range. To overcome the problems of 1S1R integration and operating  $I$ – $V$  range, selector-less ReRAMs with non-linear characteristics have been investigated.<sup>9–12</sup> It requires very simple structure of its reliable operations. The simple structure of ReRAM devices can lead reliable operation with high yield in wafer level fabrication. Although non-linear ReRAMs have an advantage of its simple structure, their non-linearity values are still insufficient, and the origin of the non-linearity was not deeply investigated. However,  $\text{TiO}_x$  has been reported to exhibit the non-linear

$I_{\text{LRS}}$  with thermally formed sub-oxide  $\text{TiO}_x$  by high compliance current.<sup>9</sup> It obtained the non-linear  $I_{\text{LRS}}$  of  $\text{TiO}_x$  by using the method of thermally activated energy.

In this study, tunnel barrier engineering of multiple-layers of  $\text{TiO}_x$  was investigated as a method to achieve the high non-linear  $I_{\text{LRS}}$ . We had controlled thickness and the oxygen profile of  $\text{TiO}_x$  tunnel barrier for high non-linearity. Optimum thickness and the oxygen profile of tunnel barrier can effectively reduce electron transfer at low voltage level with direct tunneling suppression, whereas this tunnel barrier can exhibit high current flowing at high voltage level with its Fowler–Nordheim (FN) tunneling of barrier lowering. Therefore, tunnel barrier engineering could be used to modify the defect distribution of the tunnel barrier to achieve high non-linearity and suppress  $I_{\text{LRS}}$  at  $1/2V_{\text{Read}}$ . Compared to a typical linear ReRAM, we could obtain the sufficient  $I_{\text{LRS}}$  at on-state ( $V_{\text{Read}}$ ), whereas the  $I_{\text{LRS}}$  at  $1/2V_{\text{Read}}$  could be significantly reduced by the highly non-linear tunnel barrier characteristics.

We fabricated Pt/Ti/HfO<sub>2</sub>/TiO<sub>x</sub>/Pt devices in a 250-nm via-hole structure. For the isolation layer, a 100-nm thick SiO<sub>2</sub> sidewall layer was deposited on a Pt/Ti/SiO<sub>2</sub>/Si substrate using plasma-enhanced chemical vapor deposition. Subsequently, a 250-nm via-hole was defined using the conventional KrF lithography process, followed by reactive ion etching. First, a 6-nm-thick layer of TiO<sub>x</sub> was deposited for a tunnel barrier in an Ar and O<sub>2</sub> mixed plasma using RF sputtering. To form the multiple-layers of TiO<sub>x</sub>, TiO<sub>x</sub> layer was annealed in an oxygen ambient by using rapid thermal annealing at 300 °C. This could oxidize the top surface of the TiO<sub>x</sub> layer, which formed a TiO<sub>y</sub> ( $y > x$ ) layer at the top surface of the TiO<sub>x</sub>. A 4-nm-thick HfO<sub>2</sub> layer was deposited using an atomic layer deposition system to form the main switching layer, using tetrakis(ethylmethylamino)hafnium (TEMAH) as a precursor and H<sub>2</sub>O as an oxidizer at 250 °C. The Ti oxygen reservoir and Pt top electrode (TE) were deposited using DC sputtering and defined using a 50 μm shadow mask (Figure 1).

The gray line of Figure 2(a) shows the DC  $I$ – $V$  curve, which indicates the linear characteristics of the typical ReRAM (TE/Ti/HfO<sub>2</sub>/BE). A DC bias was applied to the

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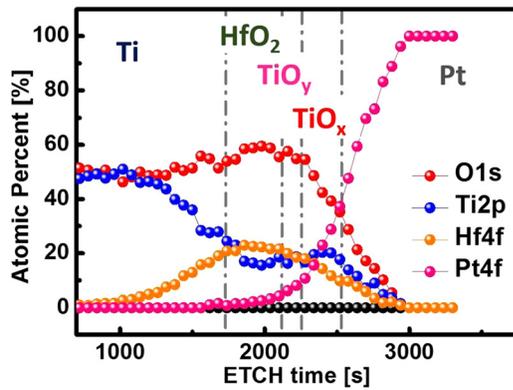


FIG. 1. XPS depth profile of selector-less ReRAM with Pt/Ti/HfO<sub>2</sub>/TiO<sub>x</sub>/Pt stacks. The multiple-layers TiO<sub>x</sub> tunnel barrier was observed.

TE, and the bottom electrode (BE) was grounded. The resistance changed from  $R_{\text{HRS}}$  to  $R_{\text{LRS}}$  with conductive filament when positive bias was applied to the TE. In contrast, the resistance changed from  $R_{\text{LRS}}$  to  $R_{\text{HRS}}$  with filament dissolution when negative bias was applied to the TE. Typical ReRAM switching is attributed to filament formation and dissolution at HfO<sub>2</sub> main switching layer. This linear ReRAM has high  $I_{\text{LRS}}$  at  $1/2V_{\text{Read}}$ , it degrades readout margin at cross-point array operation. To lower  $I_{\text{LRS}}$  at  $1/2V_{\text{Read}}$ , TiO<sub>x</sub> tunnel barrier was inserted between main switching layer and BE.

Figure 2(a) shows the DC I-V curve of the high non-linear characteristics of the selector-less ReRAM (TE/Ti/HfO<sub>2</sub>/TiO<sub>x</sub>/BE) with a 50  $\mu\text{A}$  compliance current. Non-linearity is defined using<sup>9</sup>

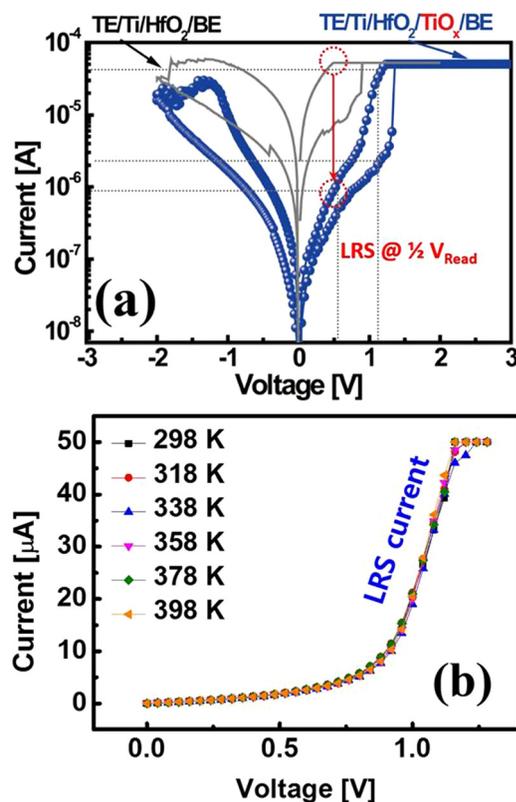


FIG. 2. (a) Typical high non-linear DC I-V switching characteristics of the selector-less ReRAM with comparison of the linear ReRAM. (b)  $I_{\text{LRS}}$  temperature independence behavior of the selector-less ReRAM with thermal stability.

$$\text{Non-linearity} = (I @ V_{\text{set}}) / (I @ 1/2 V_{\text{set}}). \quad (1)$$

Compared with a typical linear ReRAM, the tunnel barrier engineered TE/Ti/HfO<sub>2</sub>/TiO<sub>x</sub>/BE structure exhibited a substantially lowered  $I_{\text{LRS}}$  at  $1/2V_{\text{Read}}$  owing to its highly non-linear behavior. Hence, it can be applicable for cross-point array implementation without any selectors because of the high non-linearity. Figure 2(b) shows the thermal stability of the selector-less ReRAM up to 398 K. Based on this temperature independence behavior, we could confirm that the non-linearity originated with the tunnel barrier characteristics. Tunnel barriers are well known to exhibit temperatures that are not dependent on the non-linear I-V characteristics with direct and FN tunneling mechanisms as a results of the barrier height modification. Once a filament is formed in HfO<sub>2</sub>, the HfO<sub>2</sub> of the main switching layer can assume an ohmic state for the conductive filament, and the barrier height modification of the tunnel barrier mainly controls the non-linear  $I_{\text{LRS}}$  behavior. Thus, we have to consider the non-linear I-V behavior in relation to the tunnel barrier characteristics.

Hence, tunnel barrier engineering in terms of the TiO<sub>x</sub> thickness and oxygen profile control had been investigated because these characteristics modulate tunnel barrier properties in order to obtain high non-linearity in a selector-less ReRAM, as shown in Figures 3(a) and 3(c).<sup>13,14</sup> First, we could suppress the electron transfer at  $1/2V_{\text{Read}}$  along the tunnel barrier by using the optimum thickness of a tunnel barrier. However, if the tunnel barrier was too thick, it could not exhibit sufficient  $I_{\text{LRS}}$  at  $V_{\text{Read}}$  because the electron transfer was even too suppressed in high voltage level. By contrast, if the tunnel barrier was too thin, most electric field can be applied to the tunnel barrier, and high  $I_{\text{LRS}}$  can flow. It is well known that high electric field can degrade oxide reliability. It results in poor endurance reliability of ReRAM. Hence, too thick or too thin tunnel barrier could decrease the non-linearity and its yield. Thus, it was found that a 6-nm-thick TiO<sub>x</sub> tunnel barrier exhibited the highest non-linearity (Figures 3(a) and 3(b)). Next, we controlled the oxygen profile of the 6-nm TiO<sub>x</sub> tunnel barrier to achieve higher non-linearity with thermal oxidation at 300 °C (Figures 3(c) and 3(d)). Thermal oxidation could elaborately oxidize the top surface of a TiO<sub>x</sub> tunnel barrier to form a more insulating state. Thus, it could precisely determine the thickness of the oxidized layer on the top surface with thermal oxidation time. Hereafter, we define the top surface of the insulating TiO<sub>x</sub> as TiO<sub>y</sub> ( $y > x$ ). By adopting the multi-layer TiO<sub>y</sub>/TiO<sub>x</sub>, the TiO<sub>x</sub> thickness was decreased to 3.5-nm with 2.5-nm of TiO<sub>y</sub> (Figure 4(a)). Energy-dispersive X-ray spectroscopy (EDX) shows that the top surface of TiO<sub>x</sub> tunnel barrier has more oxygen content than the TiO<sub>x</sub> bulk region. Hence, the single state TiO<sub>x</sub> was changed to the multi-layer TiO<sub>y</sub>/TiO<sub>x</sub>, which contained an insulating top surface and a relatively metallic bulk region. The existence of this multi-layer TiO<sub>y</sub>/TiO<sub>x</sub> was also confirmed using an X-ray photoelectron spectroscopy (XPS) binding energy analysis of the TiO<sub>x</sub> layer (Figures 4(b) and 4(c)).<sup>15</sup> Consequently, dominant peak of Ti<sup>4+</sup>, which related to the insulating state was obtained at the top surface of TiO<sub>x</sub> tunnel barrier, and Ti<sup>2+</sup> as the relatively metallic state TiO<sub>x</sub>

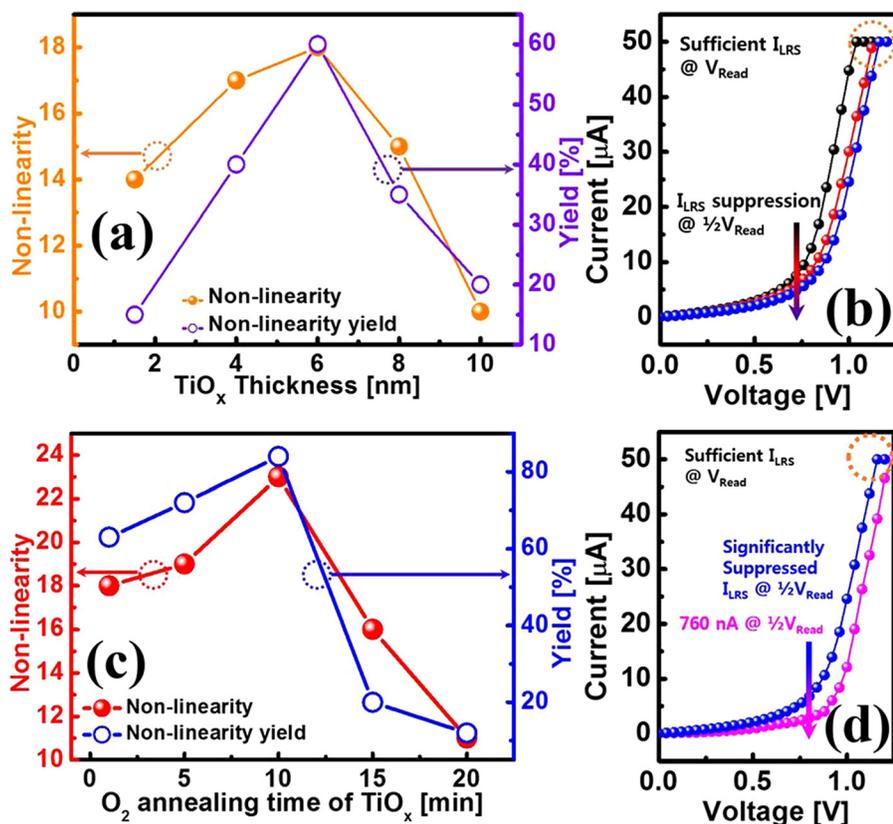


FIG. 3. (a) Non-linearity and yield improvement up to critical thickness of  $\text{TiO}_x$  tunnel barrier. (b) More suppressed  $I_{\text{LRS}}$  at  $1/2 V_{\text{Read}}$  and sufficient  $I_{\text{LRS}}$  at  $V_{\text{Read}}$ . (c) Oxygen profile dependence of non-linearity and yield. (d) Significantly suppressed  $I_{\text{LRS}}$  at  $1/2 V_{\text{Read}}$  with sufficient on-state  $I_{\text{LRS}}$ .

tunnel barrier was observed in the bulk region. Modulated  $\text{TiO}_x$  tunnel barrier exhibited a higher non-linearity and yield with multi-layer  $\text{TiO}_y/\text{TiO}_x$  by using 10 min of thermal oxidation. This tunnel barrier engineered multi-layer  $\text{TiO}_y/\text{TiO}_x$  could directly modify the defect distribution of the tunnel barrier which in turn affected the non-linearity and yield of the selector-less ReRAM (Figure 5). As shown in Figure 5, the multi-layer  $\text{TiO}_y/\text{TiO}_x$  tunnel barrier attributes the high non-linear  $I$ - $V$  characteristics with direct and FN tunneling mechanisms. In the multi-layer  $\text{TiO}_y/\text{TiO}_x$  tunnel barrier, the  $\text{TiO}_y$  and  $\text{TiO}_x$  layers play an important role for the direct tunneling suppression at low voltage level.  $\text{TiO}_y$  can suppress direct tunneling owing to its insulating state.

Furthermore,  $\text{TiO}_x$  assists to suppress electron transfer for lower current flowing at low voltage level because the thicker multi-layer  $\text{TiO}_y/\text{TiO}_x$  can reduce direct tunneling than the thin single layer  $\text{TiO}_y$ . In high voltage level,  $\text{TiO}_x$  plays an important role in high  $I_{\text{LRS}}$ . If we applied high positive bias, the  $\text{TiO}_y$  region is lowered, and FN tunneling occurred at the  $\text{TiO}_x$  layer. By contrast, the  $\text{TiO}_x$  region is lowered, and FN tunneling occurred at the  $\text{TiO}_y$  layer at high negative bias. In FN tunneling operation of the multi-layer  $\text{TiO}_y/\text{TiO}_x$ , the relatively metallic  $\text{TiO}_x$  can flow higher  $I_{\text{LRS}}$  than the relatively insulating  $\text{TiO}_y$  layer. As shown in the blue curve of Figure 2(a), the  $I_{\text{LRS}}$  of the positive and negative polarities are different owing to different FN tunneling of  $\text{TiO}_x$  and  $\text{TiO}_y$  at high voltage level, respectively. Hence, the both optimum  $\text{TiO}_y$  (2.5-nm) and  $\text{TiO}_x$  (3.5-nm) play very important roles in the high non-linear  $I_{\text{LRS}}$  with effectively suppressed direct tunneling and sufficient FN tunneling. The non-linearity is defined with  $I_{\text{LRS}}$  ratio at  $V_{\text{Set}}$  and  $1/2 V_{\text{Set}}$ . Thus, the multi-layer  $\text{TiO}_y/\text{TiO}_x$  which has both insulating and metallic states is necessary for the high non-linearity and reliability of the selector-less ReRAM.

Consequently, the multi-layer  $\text{TiO}_y/\text{TiO}_x$  could effectively suppress electron transfer at a low voltage level of  $1/2 V_{\text{Read}}$  without any cell selector device by high non-linear characteristics of  $I_{\text{LRS}}$ . In contrast, high voltage level for  $V_{\text{Read}}$  could sufficiently lower the height of the modified tunnel barrier, and electrons could transfer to the conducting  $\text{HfO}_2$  filament region by FN tunneling. By achieving reliable tunnel barrier characteristics, we could retain a sufficient  $I_{\text{LRS}}$  at  $V_{\text{Read}}$ , whereas the  $I_{\text{LRS}}$  at  $1/2 V_{\text{Read}}$  could be significantly reduced by the highly non-linear tunnel barrier characteristics. Compared to the typical linear ReRAM, the selector-less ReRAM with the multi-layer  $\text{TiO}_y/\text{TiO}_x$  tunnel

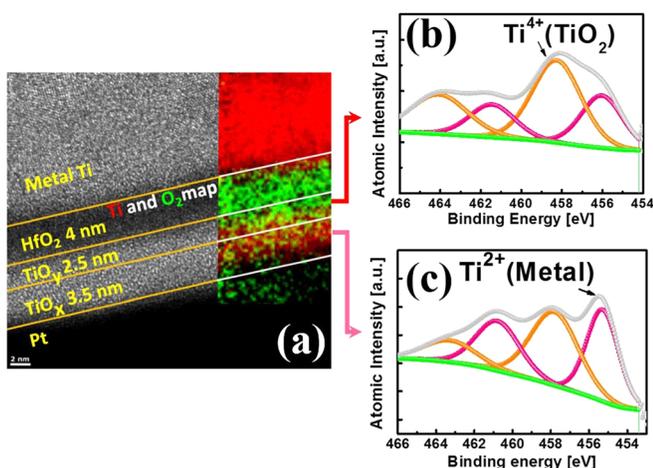


FIG. 4. (a) TEM image with oxygen and Ti distribution of EDX analysis. (b) XPS binding energy analysis for  $\text{TiO}_x$  tunnel barrier of the top surface. (c) XPS binding energy analysis for  $\text{TiO}_x$  tunnel barrier of the bulk region.

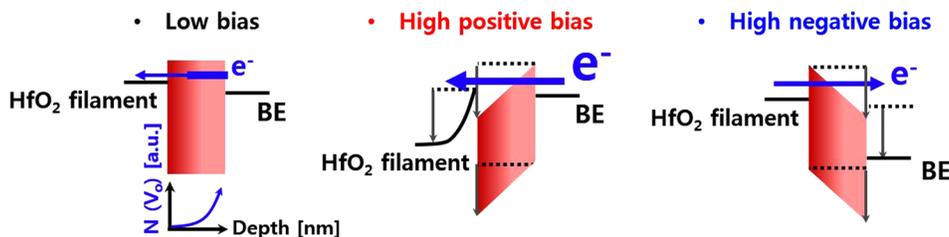


FIG. 5. Tunnel barrier height modification with applying bias and polarity. In off-state, insulating  $\text{TiO}_y$  and metallic  $\text{TiO}_x$  effectively suppress electron transfer to reduce  $I_{\text{LRS}}$ . In on-state, the lowered region of tunnel barrier can be different with applying bias polarity.

barrier exhibited excellent non-linear  $I_{\text{LRS}}$  without any cell selector device.

This research demonstrated the selector-less ReRAM ( $\sim 10$  nm thickness) with high non-linearity by the tunnel barrier engineering of the multi-layer  $\text{TiO}_y/\text{TiO}_x$ . The multi-layer  $\text{TiO}_y/\text{TiO}_x$  tunnel barrier plays a very important role in high non-linearity by the suppressed direct tunneling and sufficient FN tunneling. It could reduce  $I_{\text{LRS}}$  at off-state significantly compared with the typical linear ReRAM without degradation on-state  $I_{\text{LRS}}$ . Furthermore, It shows a promise for future high density cross-point memory applications.

This work was supported by the R&D MOTIE/KEIT (10039191) and SK Hynix semiconductor.

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